Cell Design

Standard Cells

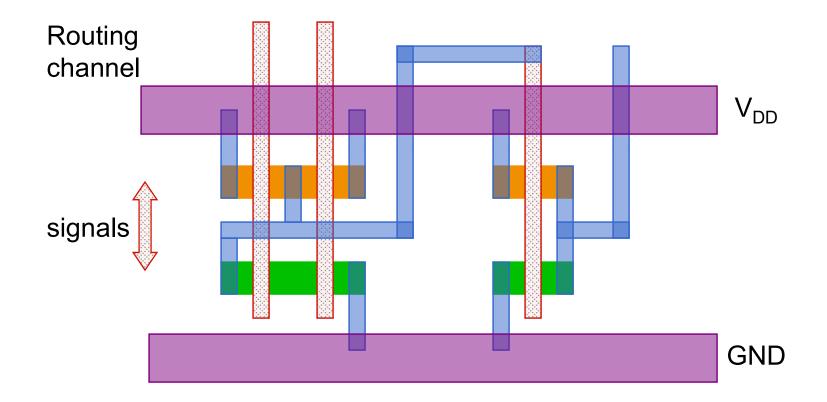
- General purpose logic
- Can be synthesized
- Same height, varying width

Datapath Cells

- For regular, structured designs (arithmetic)
- Includes some wiring in the cell
- Fixed height and width

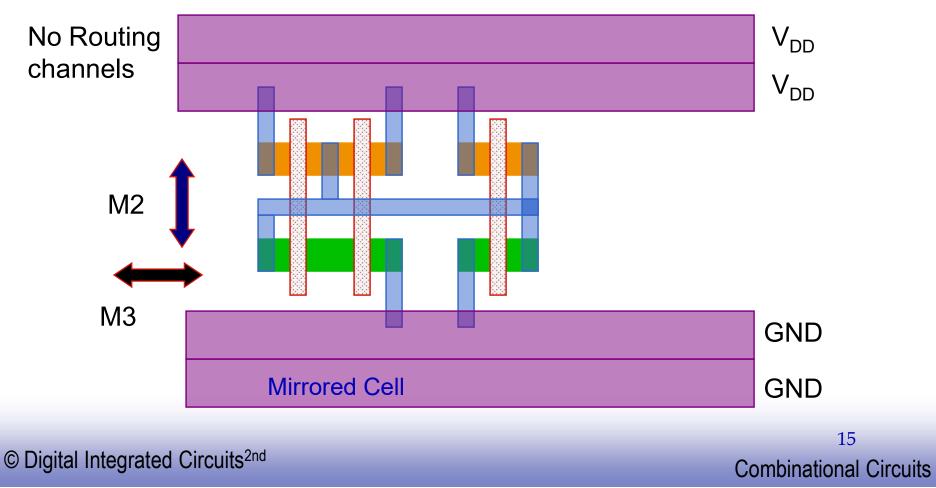


Standard Cell Layout Methodology – 1980s

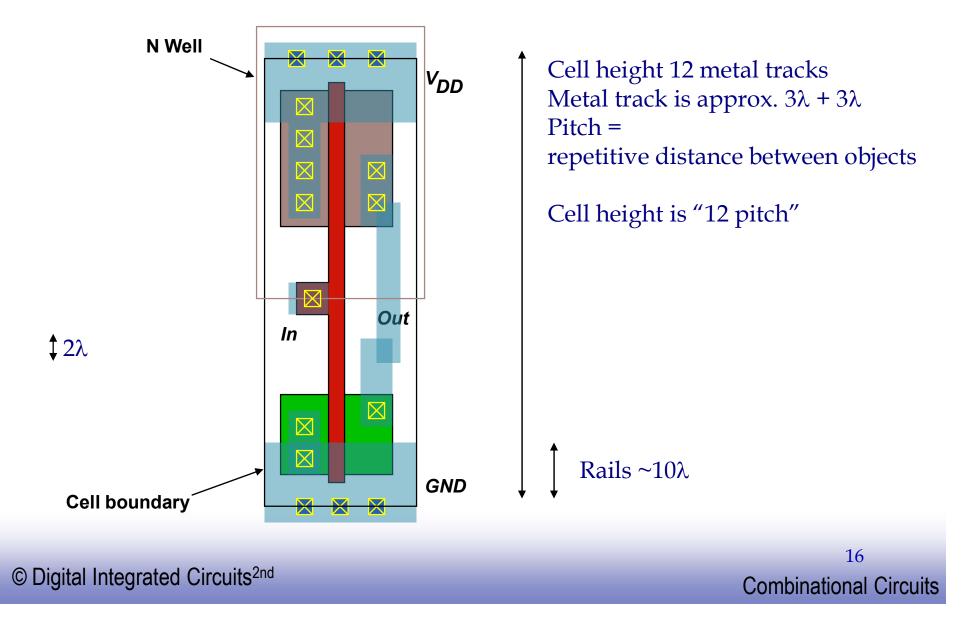


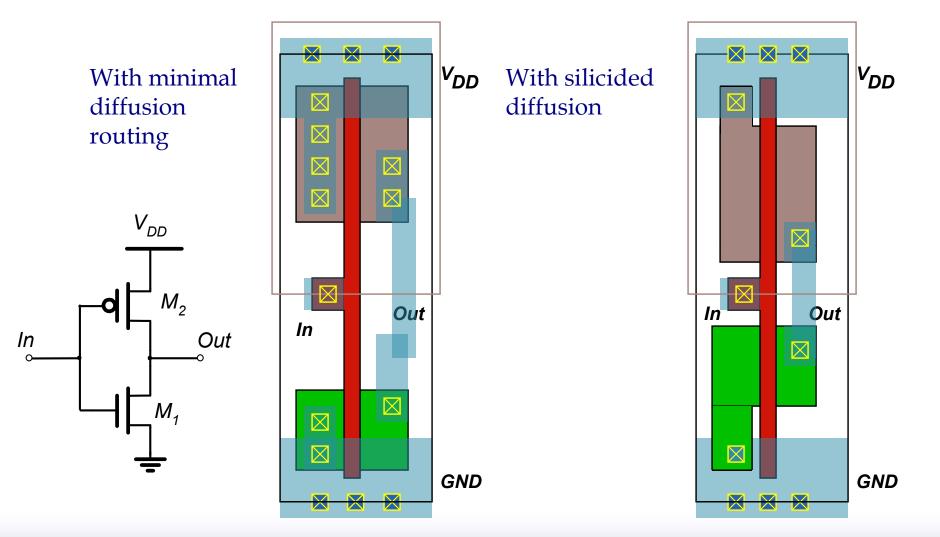
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Standard Cell Layout Methodology – 1990s

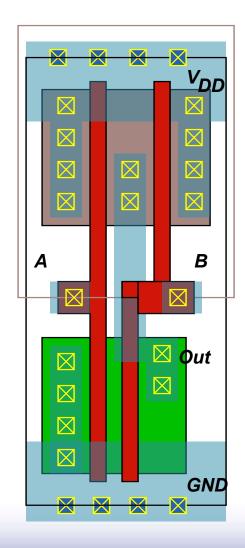


Mirrored Cell

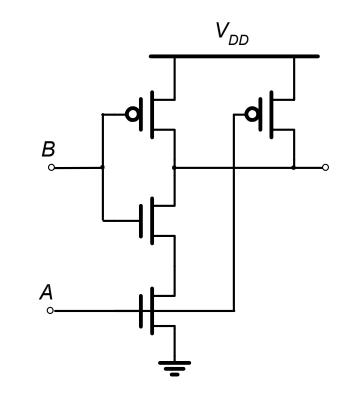




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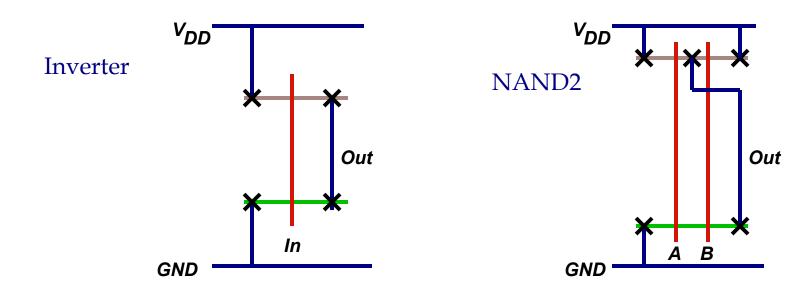
2-input NAND gate



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Stick Diagrams

Contains no dimensions Represents relative positions of transistors



Cell Design

Standard Cells

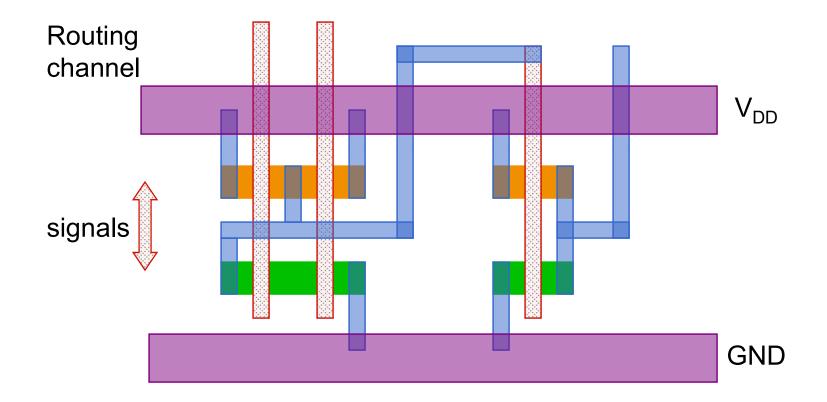
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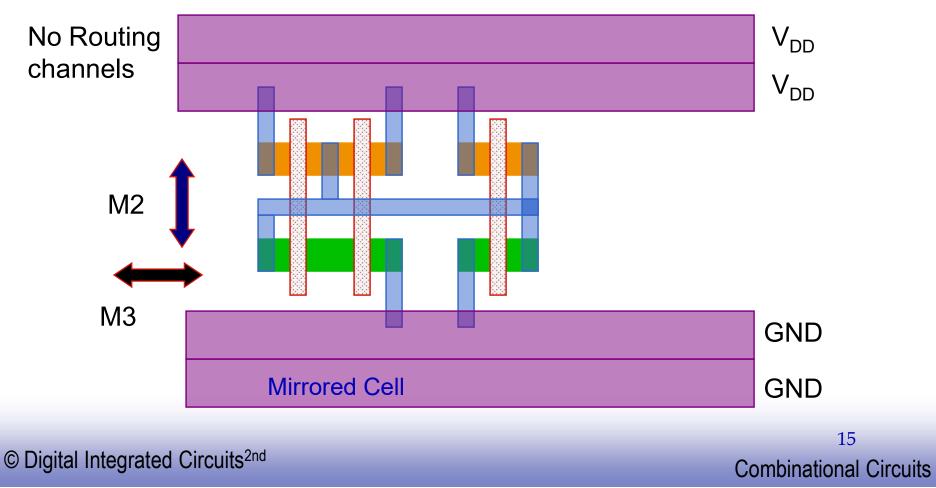


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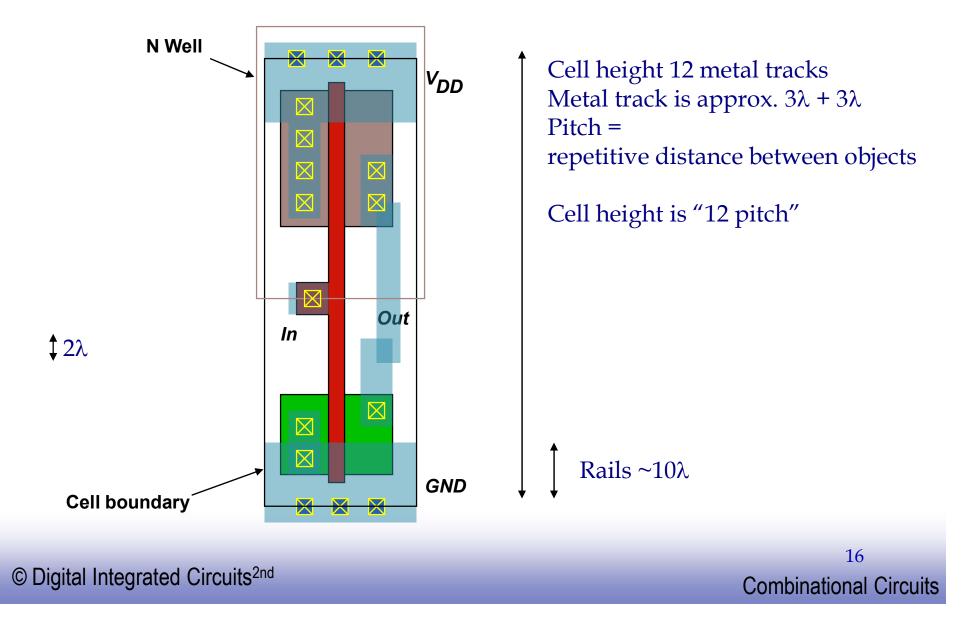


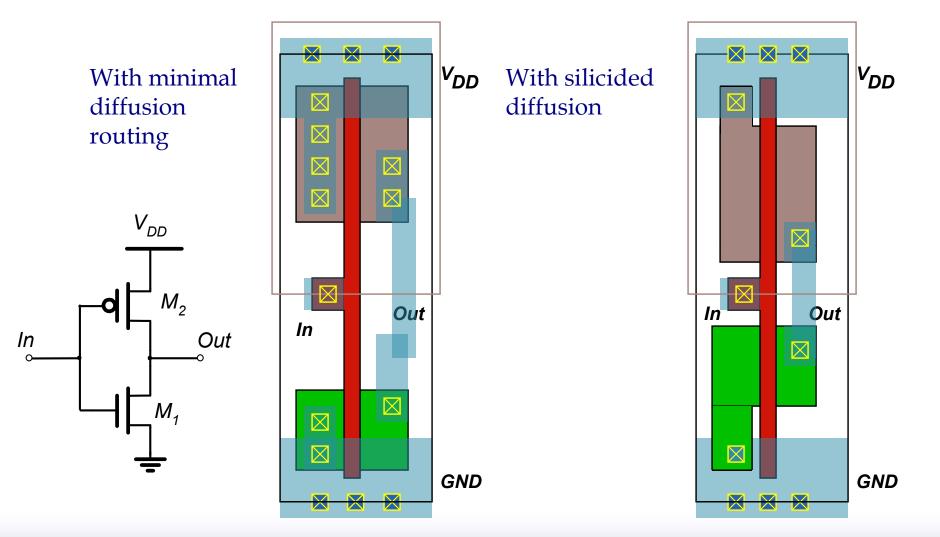
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Standard Cell Layout Methodology – 1990s

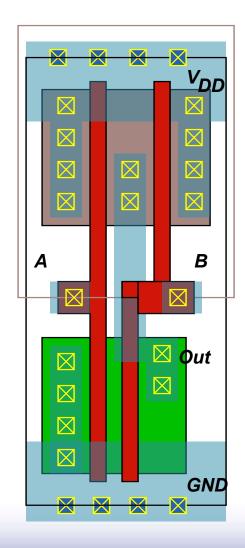


Mirrored Cell

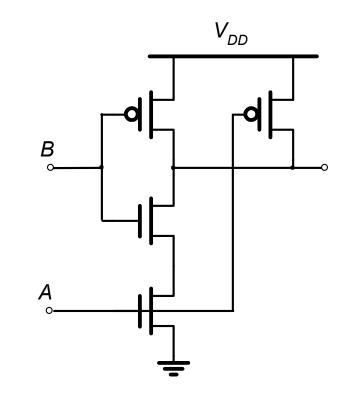




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2-input NAND gate



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